$A_3\ A_2\ A_1\ A_0$

0/1	0 0 0 D	Phase 0
2/3	0 0 1 D	Phase 1
4/5	0 1 0 D	Phase 2
6/7	0 1 1 D	Phase 3
8/9	1 0 0 D	Motor On
A/B	1 0 1 D	Drv Sel
C/D	1 1 0 D	Q6
E/F	1 1 1 D	Q7

Enable whatever drive selected

Q7	Q6	Motoron	Register Selected
0	0	0	Read all ones
0	0	1	Read Data
0	1	X	Read Status
1	0	X	Read Write-HSHK
1	1	0	Set Mode
1	1	1	WRITE DATA

Reading a register requires Q6=Q7=LO or Motoron=0

IF MOTORON, Q6=Q7=HI to read
IF MOTOROFF, Q6=Q7=LO to read

$CA0 = PH0$
$\quad 1 = PH1$
$\quad 2 = PH2$
$SEL = HDSEL$

		6	6	7	030
NBCD Dn		6	6	8	020
TAS Dn		16	16	19	020
		14	14	16	020
TAS (AN)		+2	+2	+2	
ASL Dn		5	8	10	

20	INTLV

set = doublesided

FMT BYTE has INTLV

Superdrive: Timing diagrams indicate RDY lies about status for 150 μS after issuing step. Thus seek should delay 150μS before checking for "seek done"

READY: If not up to speed, RDY always false. Can do RECAL by using Trkϕ sensor to know it's done.

Must know you were ready before post-seek routine spins on ready.

First Motor-on must be 500 mSec. This is first after ~~mount~~ insertion. Must STAY on for 500 mSec.

Max RDY after Motor-on is 600 mSec. No MIN spec.

Changing MFM ⟷ GCR can take as long as a motor-on.

CON0 —— RESET PH0
CON1 —— SET PH0
CON2 —— RESET PH1
CON3 —— SET PH1
CON4 —— RESET PH2
CON5 —— SET PH2
CON6 —— RESET PH3
CON7 —— SET PH3
CON8 —— MOTOR OFF
CON9 —— MOTOR ON
CONA —— RESET LSTRB (DR1 SEL)
CONB —— SET LSTRB (DR2 SEL)
CONC —— RESET L6
COND —— SET L6
CONE —— RESET L7
CONF —— SET L7

L7	L6	MOTOR ON	
0	0	0	READ ALL ONES
0	0	1	READ DATA REG.
0	1	X	READ STATUS
1	0	X	READ WRITE-HANDSHAKE
1	1	0	WRITE MODE REG.
1	1	1	WRITE DATA REG.